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DATE: July 14, 2005

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FROM: Pat Thomas for Robert King (512) 996-6839
(SENDER) (EXTENSION)

TOTAL NUMBER OF PAGES 5 (Including this page)

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Docket No.: SC11507ZC
Applicant: William Eric Main et al.
Serial No.: 09/773,806 746292
Art Unit: 2666
Filed: February 1, 2001
Class: 370-445000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	X	1 page Facsimile Cover Sheet
2.	X	1 page PTOL-85B Issue Fee Transmittal (in duplicate)
3.	X	2 page Comments on Statement of Reasons for Allowance

Paid by Deposit Account 503079, Freescale Semiconductor, Inc: \$1700

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.

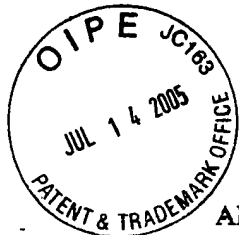
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DOCKET NO. SC11507ZC

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT William Main et al. GROUP ART UNIT: 2634
APPLN. NO.: 09/746,277 EXAMINER: Ted M. Wang
FILED: December 21, 2000 CONFIRMATION No.: 2788
TITLE: CIRCUIT AND METHOD FOR PROCESSING AN
AUTOMATIC FREQUENCY CONTROL SIGNAL

Certificate of Transmission under 37 C.F.R. 1.8

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7-14-05

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PAT THOMAS

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this
application. Entrance of the comments provided below regarding the statement of
reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079
(Freescale Semiconductor, Inc.) for any fees owed in connection with this
application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.)
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DOCKET NO. SC11507ZC

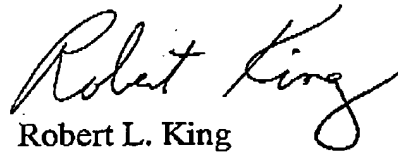
REMARKS

In a communication mailed July 1, 2005, claims 1, 5-11, 13-15 and 18-27 were allowed. The Examiner provided a statement of reasons for allowance. A particular claim recital was stated for the basis of claims 24 and 27. However, the quoted recital is not present in claim 27. Applicants disagree that "exceeding a threshold in the AFC before generating the dead band region by transferring the first and second signal to the output" is a sole, or even primary, basis for allowance of the claims in this application. Applicants submit that the collective recited combination of elements in each of the allowed claims is distinguishable over the prior art made of record, either taken alone or in combination.

SEND CORRESPONDENCE TO:

Freescall Semiconductor, Inc.
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Respectfully submitted,



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